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EFFECTS OF ROUNDOFF AND SOFT ERROR PROPAGATION IN CG ON ATTAINABLE ACCURACY

E. Agullo

joint work with

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HiePACS - Inria Project Inria Bordeaux Sud-Ouest

Forewords

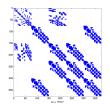
HiePACS objectives: Contribute to the design of effective tools for frontier simulations arising from challenging research and industrial multi-scale applications towards extreme computing

- Study and design of novel numerical algorithms for emerging computing platforms
- Analyse their possible weaknesses and possible remedies



Scientific context: numerical linear algebra

Goal: solving (in parallel) Ax = b, where A is a large, sparse matrix



- Appears in many academic and industrial simulation codes;
- <u>Iterative methods</u> (vs direct methods)
 - Still promising solution techniques based on Krylov subspace methods (Aleksei Nikolaevich Krylov, 1863-1945)
 - Oldest but still effective solver: the conjugate gradient method (CG) [M.R. Hestenes and E. Stiefel, JNRBS, 1952]



1. Roundoff errors: improving attainable accuracy of scalable variants (in p-CG)

2. Soft errors: impact on attainable accuracy and detection (in classical CG)



Outline

1. Roundoff errors: improving attainable accuracy of scalable variants (in p-CG)

2. Soft errors: impact on attainable accuracy and detection (in classical CG)



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1: for i = 0, ... do
       s_i := Ap_i
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      x_{i+1} := x_i + \alpha_i p_i
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9: end for
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- Recurrences ... induce accumulation of roundoff errors
- Parallel performance bottleneck: 2 distinct dot products
- Many variants designed to overcome this drawback, including
 - ► Chronopoulous-Gear CG (CG-CG) [A.T. Chronopoulous and C.W. Gear. ICAM. 1989]
 - ▶ pipelined CG (p-CG) [P. Ghysels and W. Vanroose, ParCo. 2014



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 → possible overlap of mat-vec and preconditioning with non-blocking reduction
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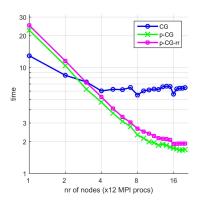
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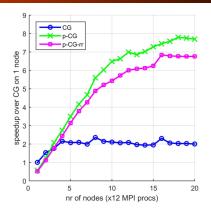
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- p-CG eventually outperforms classical CG (blue curve)
- Teasing: purple curve



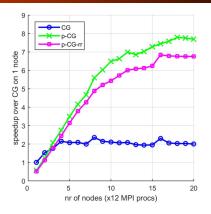
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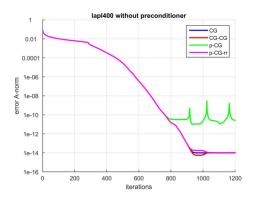
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Why shall we mind?



- Attainable accuracy of p-CG worse than classical CG
- Known/expected behaviour for three-term recurrence variants [M.H.Gutknecht, Z.Strakoš, SIMAX, 2000]



Possible remedy

 Develop a (tedious) rounding-error analysis based on known results (see e.g. N. Higham, SIAM book, 2002]) to compute the propagation of local rounding errors on the <u>deviation</u> in pipelined CG

$$\mathsf{fl}(\mathsf{a}\;\mathsf{op}\;b) = (\mathsf{a}\;\mathsf{op}\;b)(1+\epsilon), \quad |\epsilon| \leq \mathsf{u}$$

$$f_{i+1} = (b - A\bar{x}_{i+1}) - \bar{r}_{i+1}$$

$$= b - A(\bar{x}_i + \bar{\alpha}_i\bar{p}_i + \delta_i^x) - (\bar{r}_i - \bar{\alpha}_i\bar{s}_i + \delta_i^r)$$

$$= f_i - \bar{\alpha}_ig_i - A\delta_i^x - \delta_i^r$$

 Design a residual replacement strategy based on a practical estimate [H. van der Vorst, Q. Ye, SISC, 2000]

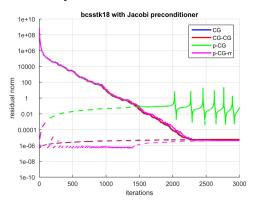
$$||f_i|| \le \sqrt{\mathbf{u}} ||\bar{r}_i||$$
 and $||f_{i+1}|| > \sqrt{\mathbf{u}} ||\bar{r}_{i+1}||$.



Features of the new algorithm (p-CG-rr)

At a negligible extra computational cost:

Attainable accuracy is recovered

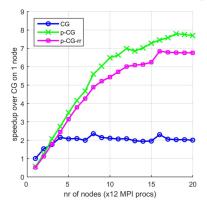




Features of the new algorithm (p-CG-rr)

At a negligible extra computational cost:

- Attainable accuracy is recovered
- Parrallel performance not "much affected" (purple vs green)

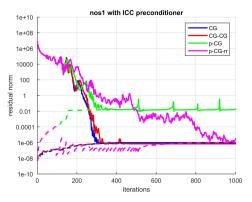




To be continued!

A few still open questions

- Analysis of the convergence delay
- Relax some theoretical hypothesis that might not hold in practice





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What is soft error?

- Possible causes : voltage reduction, electricity fluctuations, cosmic particle effects, etc...
- Appears on: memories, registers, pipeline of the processor

Extreme scale platforms



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OF COMPONENTS



AREA EFFECTED BY RADIATION



How soft errors occur?

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2: $s := Ap_i$
3: $\alpha := r_i^T u_i / s^T p_i$
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We consider transient soft errors

- in the most computationally expensive kernels
- other "cheaper" kernels could be protected by redundancy



Soft errors in this study

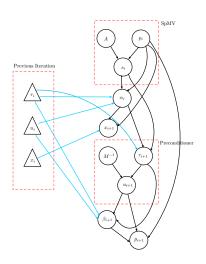
Elliott, Hoemmen, Mueller. 2013'

- "Transient faults occur once, and while the fault is transient the effect of the fault may be persistent."
- SDC. "In this work, we address a very specific type of fault, i.e., a fault that silently introduces bad data, while not persistently tainting the data that was used in the calculation. For example, let a=2 and b=2, then c=a+b=10, while simplistic, this model presumes no knowledge of the nature of the fault, only that c is incorrect."



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$$r_0 := b - Ax_0$$

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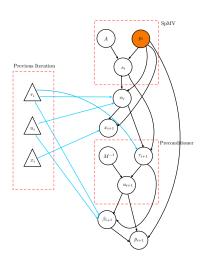




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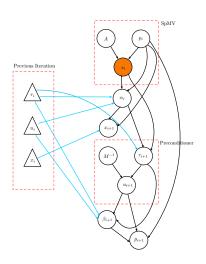
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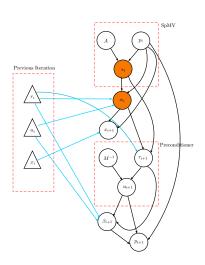
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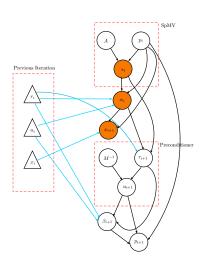
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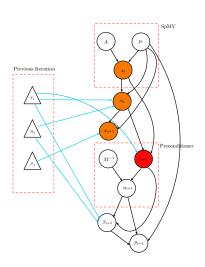
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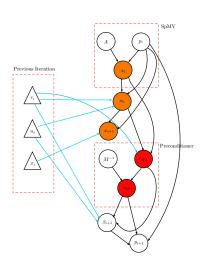
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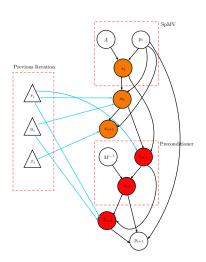
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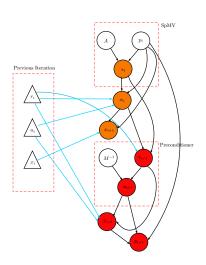
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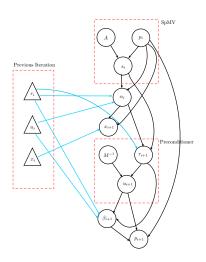




Propagation of SDC in preconditioner application

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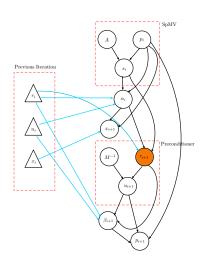
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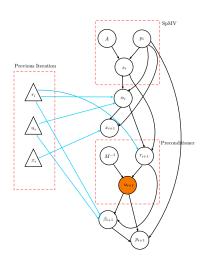
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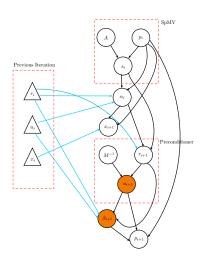
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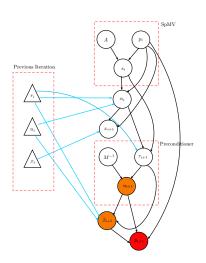
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9: β := $(r_{i+1}, u_{i+1})/(r_i, u_i)$
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11: **end for**





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$$r_0 := b - Ax_0$$

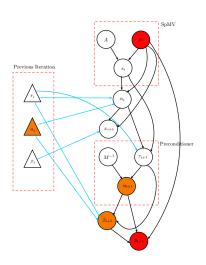
2: $u_0 := M^{-1}r_0$; $p_0 := r_0$
3: **for** $i = 0, ...$ **do**
4: $s := Ap_i$
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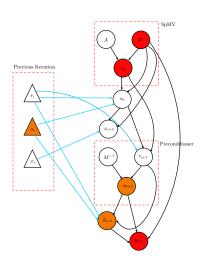
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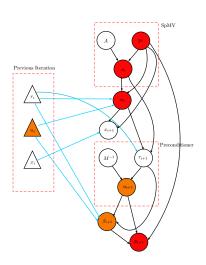
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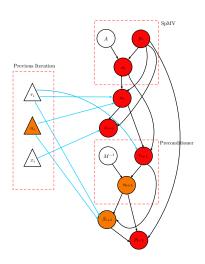
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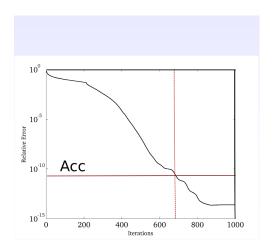


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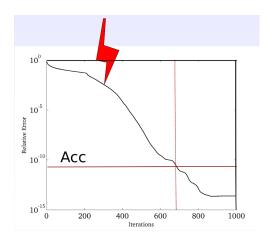






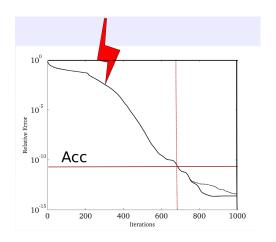
	Effect on the convergence
Converged	
Not Converged	





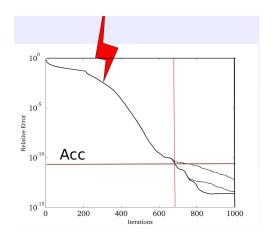
	Effect on the convergence
Converged	
Not Converged	





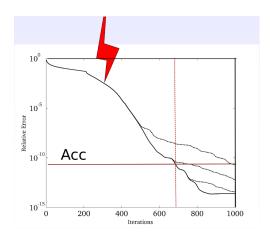
	Effect on the convergence
Converged	
Not Converged	





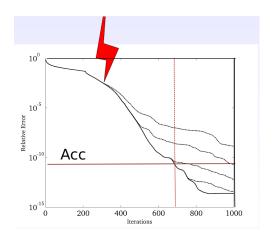
	Effect on the convergence
Converged	
Not Converged	





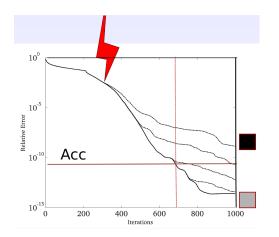
	Effect on the convergence
Converged	
Not Converged	





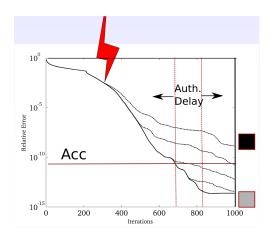
	Effect on the convergence
Converged	
Not Converged	





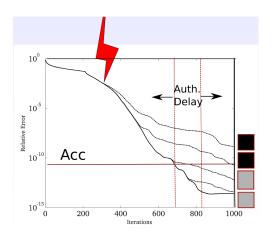
	Effect on the convergence
Converged	
Not Converged	





	Effect on the convergence
Converged	
Not Converged	

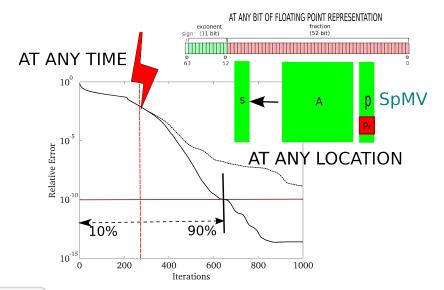




	Effect on the convergence
Converged	
Not Converged	

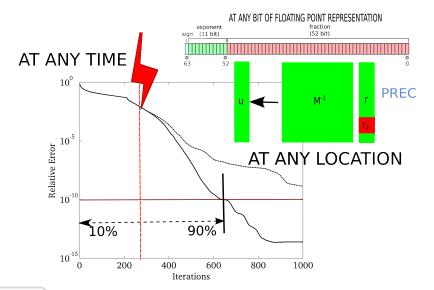


Fault injection methodology in 64-bit



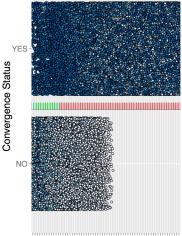


Fault injection methodology in 64-bit





Sensitivity to soft-errors in mat-vec



Fault Injection Time

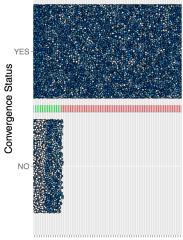
- 0 10%
- 0 20%
- 0 30%
- o 40%
- 0 000/0
- 9 709
- 80%
- 90%

- Many soft-errors are silent
- Mainly early bit-flip on high order bits are critical when computing $s_i = Ap_i$

Flipped Bit



Sensitivity to soft-errors in precond



Fault Injection Time

- 0 10%
- 0 20%
- 30%40%
- 40%50%
- 60%70%
- 80%90%

- Even many more soft-errors are silent
- Mostly bit-flip in sign/exponent are critical when computing

$$u_{i+1} := M^{-1} r_{i+1}$$

Flipped Bit

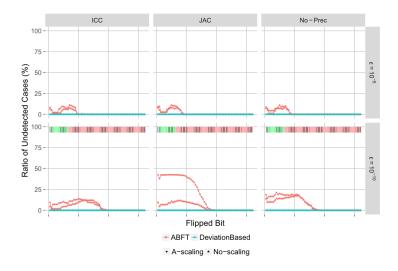


Detection criteria

- ABFT [Huang & Abraham, IEEE TOC, 1984] ("Note: since there may be roundoff errors for floating point operations, a small tolerance should be allowed for in the the comparison")
- Deviation-based (DN) [Vorst & Yee, SISC, 2000]
- α -based (alpha): $(\lambda_{\max}^{-1} \leq \alpha_i \leq \lambda_{\min}^{-1})$ [Hestenes & Steifel, JNRBS, 1952] (a safe interval for α parameter for distinguishing "normal rounding-off errors" from "errors of the computer" (sic!))

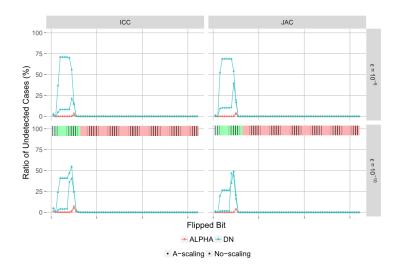


Detection robustness v.s. SpMV bit-flip location





Detection robustness v.s. Preco. bit-flip location





Main Observations

Detection

- Deviation is a good criterion candidate for SpMV faults but not for preconditioner faults
- Control frequency for deviation should be investigated
- α criterion works well for preconditioner faults but not for SpMV
- An estimation of extremal eigenvalues of preconditioned matrix is needed for α criterion (often known for scalable preconditioners)



Acknowlegement for financial support:

French ANR: RESCUE project

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○ G8 : ECS project



More information on http://hiepacs.bordeaux.inria.fr/

Merci for your attention Questions?

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